

**SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR MAPPING
SYSTEM MEMORY IN A MULTIPLE NODE INFORMATION HANDLING SYSTEM**

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Abstract of Disclosure

A system, method, and computer program product for detecting a first memory in a first node and detecting a second memory in a second node coupled to the first node. The system, method, and computer program product ensure that a first set of contiguous addresses is mapped to a portion of the first memory where the first set of contiguous addresses each have a value lower than a four gigabyte address, and ensure that a second set of contiguous addresses is mapped to a portion of the second memory where the second set of contiguous addresses each have a value lower than the four gigabyte address.

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